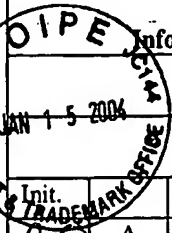


Form PTO 1449 (Rev. 2-32)		U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. IMPJ-0003D1		Serial No.: 10/661,037	
 Information Disclosure Statement by Applicant (Use several sheets if necessary)				Applicant: John D. Hyde et al.			
				Filed: September 12, 2003 Group: <u>Unassigned</u> 2822			
<b>U.S. Patent Documents</b>							
Init.		Document No.	Date	Name	Class	Subclass	Filing Date
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	D	5,376,935	12/27/1994	Seligson	341	136	3/30/1993
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<b>Foreign Documents</b>							
							Translation
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							No
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	N	Bastos et al., "A 12-Bit Intrinsic Accuracy High-Speed CMOS DAC", December 1998, IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, pp. 1959-1969.					
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	R	Vittoz, "Dynamic Analog Techniques", Design of Analog-Digital VLSI Circuits for Telecommunications and Signal Processing, Chapter 4, 1994, pp. 97-124.					
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Examiner <u>K. A. Edwards</u>					Date Considered <u>1-25-05</u>		
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.							

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